Notice of Allowability	Application No.	Applicant(s)	
	09/825,889	WAKISAKA ET AL.	
	Examiner	Art Unit	
	Hai C Pham	2861	pw
The MAILING DATE of this communication apperall claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	ears on the cover sheet with (OR REMAINS) CLOSED in to or other appropriate commun GHTS. This application is sul and MPEP 1308.	his application. If not include ication will be mailed in due o	d course. THIS
1. This communication is responsive to Amendment filed 01/2	<u>20/04</u> .		
2. The allowed claim(s) is/are <u>1-31</u> .			
3. $igotimes$ The drawings filed on <u>06/19/04, 10/31/02 & 06/10/03</u> are a	ccepted by the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)			
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	6. ☐ Interview Sur Paper No./N 08), 7. ☐ Examiner's A	ormal Patent Application (PTC mmary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Allo	

1. The following is an examiner's statement of reasons for allowance: claims 1, 8, 9 are patentable over the prior art patents and printed publications because of the specific arrangement of the plurality of light-emitting device array chips on a substrate in a straight line or in a staggered layout so as to oppose to a rod lens array, each of the light-emitting device array chips having a light-emitting device array, wherein the rod lens array, the substrate supporting member for supporting the substrate and the driver circuit board are each secured directly to a support member for use as an optical write head, is not taught by the art of record alone or in combination.

Claims 10, 16-18 and 21 are patentable over the prior art patents and printed publications because of the specific arrangement of the plurality of light-emitting device array chips on a substrate in a straight line or in a staggered layout so as to oppose to a rod lens array, each of the light-emitting device array chips having a light-emitting device array, wherein the light-emitting device array chips are mounted directly on a flexible printed circuit sheet interposed between the substrate and the light-emitting device array chips, which is not taught by the art of record alone or in combination.

Claims 2-7, 11-15, 19-20, 22-29 and 30-31 are allowed because they are directly or indirectly dependent from claims 1, 8-10, 16-18 and 21 above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

Application/Control Number: 09/825,889 Page 3

Art Unit: 2861

accompany the issue fee. Such submissions should be clearly labeled "Comments on

Statement of Reasons for Allowance."

2. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Hai C Pham whose telephone number is (571) 272-

2260. The examiner can normally be reached on M-F 8:30AM - 5:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Stephen D Meier can be reached on (571) 272-2149. The fax phone

number for the organization where this application or proceeding is assigned is 703-

872-9306.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

PRIMARY EXAMINER

Harlotham

April 12, 2004